

PATENT Docket No. INTEL/17586

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):)	I hereby certify that this paper and the documents referred to as enclosed
Shi et al.)	therewith are being deposited with the United States Postal Service as first
U.S. Serial No.: 10/684,690))	class mail, postage prepaid, in an envelope addressed to Commissioner
For: "Methods and Apparatus to Analyze Escape Analysis of An Application")	for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date:
Filed: October 14, 2003)	February 7, 2005
Assignee: Intel Corporation)	2011
Group Art Unit: 2171)	Mark G. Hanley Attorney for Applicant(s)
Examiner: Not Yet Assigned	ć	Registration No. 44,736

STATUS LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced matter. Please note that it has been more than nine months since the last communication.

Respectfully submitted,

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February 7, 2005

By:

Mark G. Hanley

Registration No.: 44,736